

S7 DPU Products

High Performance Programmable DPU 1/10/25/40/50/100/200GbE Controller

Enables several offloads, programmable compute, encryption, virtualization over a single wire.

Highlights

- Full suite of Storage features
- Full suite of Cloud features
- Full suite of data center networking features
- Full suite of data streaming features
- Full suite of encryption functions
- Embedded programmable DPU
- Integrated Ethernet switch
- Software Compatible with T4, T5, and T6

Applications

Datacenter Networking

- Scale out servers and NAS systems
- Consolidate LAN, SAN, and cluster networks (run InfiniBand and FibreChannel applications on Ethernet)
- Enhanced network and server security

Cloud Computing

- Virtualization features to maximize cloud scaling and utilization
- Cloud-ready functional and management features
- Secure Sockets offload
- Full support for overlay products

Networked Storage

- Develop high-performance shared-storage systems providing both file and block level services
- Integrated encryption support
- NVMe Fabrics (iWARP & RoCEv2)
- NVMe/TCP
- Very high data-integrity

High Performance Computing

- Very low latency Ethernet
- High performance RDMA support
- Increase cluster fabric bandwidth

Streaming Applications

- Internet attack protection
- QoS and Traffic Management
- Video streaming

Edge Products

- Micro Servers
- Gateways
- 5G Appliances
- Firewalls

Overview

Chelsio's S7 is a quad port 1/10/25/50/100Gb or dual port 40/100/200Gb Ethernet Unified Wire DPU ASIC with a PCI Express 5.0 host bus interface, optimized for storage, cloud computing, HPC, virtualization, security, AI, and other datacenter networking applications.

The seventh generation S7 ASIC technology from Chelsio provides the highest performance and efficiency, with dramatically lower host system CPU communications overhead. Thanks to on-board

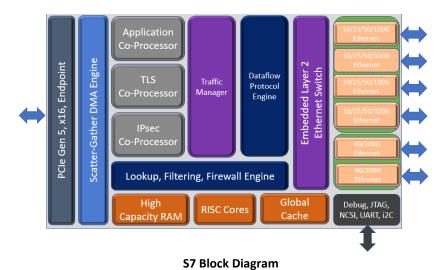
communications overhead. Thanks to on-board hardware, that offloads TCP/IP, UDP/IP, Unified RDMA (RoCEv2 & iWARP), iSCSI, NVMe-oF, NVMe/TCP, NVGRE, VXLAN, and TLS/IPsec processing from its host system and frees up host CPU cycles for the user applications. As a result, the system benefits from higher bandwidth, lower latency, and reduced power consumption.

S7 runs the predecessor T4, T5, & T6 silicon software without modification to enable leveraging of the user's existing software investment.

S7's architecture is Chelsio's 7th generation DPU technology road-tested across several tier-1 OEMs over the years and has evolved to support all offloads using host memory. As a result, S7 technology can now enable a full featured DPU technology in a small memory-free package to address server and cloud applications at an aggressive price point.

The Memory-free Operation

S7's architecture allows using the host memory for performing various offload functions, removing the need for card memories. The system can benefit from host CPU savings, with no additional cost compared to regular stateless offload NIC, resulting in an extremely lower cost bill of materials. Up to 2048 offload connections can be cached on S7 concurrently, making it an ideal choice for Client/Initiators. It uses the exact same firmware and software that runs on T7 (with card memory).



Features

	S74	S72	
Host Interface			Storage
PCI Express Gen5 x16	~	~	iSCSI initiat
End Point (EP) operation	~	~	T10 DIF/DI
MSI-X, MSI, legacy pin interrupts	~	~	NVMe-oF C
			NVMe-oF C
Wire Interface			NVMe/TCP
NRZ or PAM4	~	~	iSER
2x1/10/25/40/50/100G		~	Data-at-res
4x1/10/25/50/100G or 2x40/100/200G	~		
IEEE 802.3cd (50/100/200GbE)	~		Security
IEEE 802.3bs 200GbE	~		AES 128/25
IEEE 802.3by 25GbE	~	~	TLS and IPs
IEEE 802.3bm 40GbE/100GbE	~	~	TLS and IPS
IEEE 802.3ba (40/100GbE)	~	~	QUIC co-pr
IEEE 802.3bj (100 GbE over copper/backplane)	~	~	QUIC inline
IEEE 802.3az Energy Efficient Ethernet	~	~	Inline IPsec
IEEE 802.3ap Backplane Ethernet	~	~	Secure firm
IEEE 802.3ae (10 GbE)	~	~	Hardware
IEEE 802.3z (1GbE)	~	~	
IEEE 802.1p Priority	~	~	Cloud & Vi
IEEE 802.1Q VLAN Tagging	~	~	Inband Tele
IEEE 802.1Qbg EVB/VEPA	~	~	NVGRE, VX
IEEE 802.1BR Bridge Port Extension	~	~	PCI-SIG SR-
IEEE 802.1Qau Congestion Notification	~	~	264 port vi
IEEE 802.1Qbb PFC	~	~	EVB, VEPA,
IEEE 802.1Qaz (ETS)	~	~	512 MAC a
IEEE 802.3x Flow Control	~	~	NAT Offloa
IEEE 802.3ad Load-balancing and Failover	~	~	
Ethernet II and 802.3 encapsulated frames	~	~	Stateless O
Multiple MAC addresses per interface	~	~	TCP/UDP c
Jumbo Frames up to 9.6 Kbytes	~	~	TSO, LSO, a
ITU-T G.8262, Sync-E	~	~	VLAN filter
IEEE 802.1AS Timing and Synchronization	~	~	Packet filte
IEEE 1588 PTP	~	~	Nanosecor
			Ethernet R
Streaming			Packet Tra
Integrated Traffic Management	~	~	Adaptive in
Advanced QoS support	~	~	Receive sid
Hierarchical QoS	~	~	
			Embedded
			400Gb DPU

	S74	S
Storage		
iSCSI initiator and target mode stack	~	
T10 DIF/DIX support for iSCSI	~	
NVMe-oF Offload (iWARP)	~	
NVMe-oF Offload (RoCEv2)	~	
NVMe/TCP Offload	~	
ISER	~	
Data-at-rest encryption	~	
Security		
AES 128/256 and SHA1/SHA2 offload	~	
TLS and IPsec co-processor mode	~	
TLS and IPSec inline mode	~	
QUIC co-processor mode	~	
QUIC inline mode	~	
Inline IPsec & TLS for all Offload Traffic	~	
Secure firmware update	~	
Hardware Root of Trust support	~	
Cloud & Virtualization		
Inband Telemetry	~	
NVGRE, VXLAN and GENEVE support	~	
PCI-SIG SR-IOV, 256 VF, 8 PF	~	
264 port virtual switch	~	
EVB, VEPA, Flex10, VNTag	~	
512 MAC addresses	~	
NAT Offload	~	
Stateless Offloads		
TCP/UDP checksum offload for IPv4 & IPv6	~	•
TSO, LSO, and GSO for IPv4 & IPv6	~	•
VLAN filtering, insertion & extraction	~	•
Packet filtering and attack protection	~	•
Nanosecond granularity 64b timestamping	~	•
Ethernet Routing (packet header rewrite)	~	•
Packet Tracing and Packet Sniffing	~	•
Adaptive interrupt coalescing	~	•
Receive side scaling (RSS)	~	•
Embedded Processors		
400Gb DPU Core	~	

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	\$74	\$7
High Performance RDMA		
Native RoCEv2 support	~	
Native iWARP support	~	
All to All support	~	•
TCP & UDP Offload		
Full TCP stack including IPv4 & IPv6	~	•
Extensive RFC compliance, fully featured	~	•
VLAN support up to 4096 VLAN IDs	~	•
Load balancing and failover capabilities	~	•
UDP Sockets API	~	
Low user-to-user latency	~	
Multicast replication on ingress or egress	~	
Patented Seamless Failover	~	
Proxy Switching	~	
High capacity offload without card memory	~	•
Data Center Features		
Internet Attack Protection	~	•
PFC, DCB, CEE	~	
Time stamping support	~	
Flow mirroring, sampling and statistics	~	
GPUDirect	~	
GPUDirect Storage (GDS)	~	
Management and Other Interfaces		
UART	~	
NC-SI	~	
SPI Flash	~	
I2C, MDIO, GPIO, JTAG	~	,
PLDM, MCTP (SMBus or PCIe), RBT	~	
SGMII for 1Gb BMC interconnect	~	
JTAG IEEE 1149.1 and IEEE 1149.6	~	
SyncE	~	•
- / -		
Boot Facilities ISCSI, PXE, UEFI	<i>.</i>	

Physical & Environmental

• Operating Humidity: 5 to 95%

• Operating Temp: -40° to 55° C or -40° to 131° F

• Fully RoHS Compliant

Ordering Information

	S72ASIC	S74ASIC	
Card Memory	No		
Conn. Capacity	Up to 500k		
Cached conns.	2k		
200Gb Typ Power*	8W	14W	
200Gb WC Power*	15W	19W	
Package Size (0.8mm pitch)	21mm	21mm	

* Configuration dependent

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