

## Highlights

- PCI Express Gen3 x8
- Low latency user-space I/O
- 32K concurrent offloaded conn. capacity
- Full TCP and UDP offload Full iSCSI, FCoE offload
- Full NVMe over Fabrics (NVMe-oF) offload
- Full iWARP RDMA offload
- OVS Offload with OpenFlow support
- DPDK (Data Plane Development Kit)
- EVB, Flex10, VNTag
- PCI-SIG SR-IOV
- Integrated media streaming offload
- HW based firewall and NAT offload
- Traffic filtering & management
- IEEE 1588v2 Precision Time Protocol
- Software Compatible with T4 and T5
- Supports x86, Armv8 (Aarch64), IBM Power and OpenPOWER Servers

## Applications

#### **Datacenter Networking**

- Scale out servers and NAS systems
- Consolidate LAN, SAN and cluster networks
- Enhanced network and server security

## Cloud Computing

- Virtualization features to maximize cloud scaling and utilization
- Runs InfiniBand, Fibre Channel applications unmodified over Ethernet
- Cloud-ready functional and management features
- QoS and Traffic Management

#### **Networked Storage**

- Enable high performance NAS systems and Ethernet-based SANs
- Develop shared-storage systems providing both file and block level services
- Build high performance storage backend fabrics using Ethernet

### **High Performance Computing**

- Very low latency Ethernet
- High performance RDMA support
- Increase cluster fabric bandwidth
- Deploy Ethernet-only networking for cluster fabric, LAN and SAN

## **Specifications**

## Host Interface

- PCI-E Gen3 x8
- MSI-X, MSI and support for legacy pin interrupts

## Virtualization

- PCI-SIG SR-IOV
- 128 Virtual and 8 Physical functions
- 140 port virtual switch
- OVS Offload
- EVB, VEPA, Flex10, VNTag
- 512 MAC addresses

## NVGRE, VXLAN and GENEVE support

# **T540-LP-CR**

# **High Performance, Quad Port 10GbE Unified Wire Adapter**

Enables TCP, UDP, iSCSI, iWARP, FCoE, Offload over Single Unified Wire with SR-IOV, EVB/VNTag and DCB

## **Overview**

Chelsio's T540-LP-CR is a guad port low profile, 10 Gigabit Ethernet Unified Wire adapter with a PCI Express 3.0 host bus interface. The adapter is optimized for computing, storage, cloud HPC, virtualization other datacenter and applications.

This adapter, based on the fifth generation (T5) technology from Chelsio provides the highest performance available and dramatically lowers host-system CPU



communications overhead with on-board hardware that offloads TCP/IP, iSCSI, FCoE and iWARP RDMA processing from its host system and frees up host CPU cycles for other applications. As a result, the system benefits from higher bandwidth, lower latency and reduced power consumption.

A large portion of offloads enabled by T540-LP-CR are based on standard TCP, IP, UDP protocols (such as iSCSI and iWARP), and thus can operate with a software peer, or be replaced with a software solution at lower performance, thus providing the requisite reliability for enterprise customers, and allowing incremental installs in the datacenter. It will work with any legacy switch infrastructure and does not rely on new features such as DCB, PFC, ETS, etc.

T540-LP-CR supports IEEE standards-based link aggregation/failover features, as well as inter-adapter failover techniques that make it ideal for critical network applications requiring redundancy and high-availability capabilities. It also includes an integrated Traffic Manager and a built-in eSwitch for robust network traffic flow control, traffic classifier, load balancer, firewall capabilities at the port level.

FCoE and iSCSI support in T540-LP-CR benefit from high reliability features that include memory ECC, data path CRC and T10-DIX offload, in addition to the checksums and CRC available at different protocols layers. High performance iSCSI and FCoE provide a drop-in replacement upgrade from legacy SANs to converged networks.

## The Unified Wire Solution

The T540-LP-CR enables a unified wire for LAN, SAN and cluster applications, built upon a high bandwidth and low latency architecture along with a complete set of storage and cluster protocols operating over Ethernet (iSCSI, NVMe-oF, FCoE and iWARP). A unified wire means having the ability to utilize all offload or non-offload protocols at the same time, over the same link, using the exact same firmware, host software, and adapter. Thus, a given system using T540-LP-CR can be easily targeted at different vertical markets and a variety of useful functions enabled (i.e. dial the bandwidth to a given application and assign access control, while offloading only part of the traffic). The Ethernet-only networking thus reduces the infrastructure costs in network adapters, cables, switches, rack space, power, equipment spares, management tools, planning, networking staff and installation.

# **Fifth-Generation Protocol Offload Engine**

The T5 is Chelsio's fifth-generation TCP offload (TOE) design, fourth-generation iSCSI design, and third-generation iWARP (RDMA) implementation. With support for the 8GT/s PCIe Gen 3 data rate, it provides 64 Gbps of raw bandwidth to the server. It also provides support for PCIe SR-IOV virtualization with embedded virtual switch.

# Complete and Flexible TCP Offload

The T540-LP-CR transport engine executes programmable firmware and is configurable with hundreds of registers for protocol parameters, RFC compliance and offload control. It can offload protocol processing per connection, per-server, per-interface, while simultaneously providing complete stateless offload for nonoffloaded connections (processed by operating systems stack running on host CPU). It also provides a flexible direct data placement capability for regular TCP sockets, with all the benefits of zero-copy and kernel bypass without rewriting the applications.

## Packet Switching and Routing

The T540-LP-CR integrates a 140-port high performance L2-L3 packet switch with integrated access control and flow control support, which allows switching traffic from any of the ports or host queues or physical or virtual functions to each other. The switch can further provide multicast and replication functions in ingress or egress direction. Typical use is for very high performance OVS offload.

## **Robust, Proven Solution**

Subjected to thousands of hours of compatibility testing, over a decade of stress testing by several OEM test suites and production deployment in servers, storage systems, and cluster computing, Chelsio's robust, stable protocol offload technology delivers proven performance in a wide range of environments.

## Software Drivers

Chelsio offers a full suite of protocol software drivers with the T540-LP-CR adapters. See https://www.chelsio.com/support/ for the latest information.

## **Ordering Information**

Model: T540-LP-CR Physical Interface: 10GBASE-SR/LR\* Connector: SFP+ Media: MMF/SMF fiber or twinax (up to 5m)

## Accessories

SM10G-SR/LR: TAPCABLE-1M/3M/5M: SRCABLE3M/LRCABLE3M:

10G short/long reach SFP optical module Twinax/DAC passive cable for 10Gb, 1M/3M/5M Short/Long reach fiber optics cable for 10Gb, 3M

\*SFP+ optics sold separately. Only Chelsio-supplied modules may be used.

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## High Performance RDMA

- Low latency and line rate bandwidth
- Enhanced RDMA primitives including Atomics & Immediate data
- iWARP support in standard OFED
- Microsoft Network Direct support
- Native support for Windows Server 2012-R2, 2016, Azure Stack, Storage Replica, Storage Spaces Direct, Client RDMA, SMB-Direct, Network Direct, PacketDirect, VMMQ/vRSS
- Support for iSER, NFS-RDMA, Lustre-RDMA, NVIDIA's GPU-Direct, Hadoop-RDMA

#### TCP/IP Full Offload

- Full TCP stack including IPv4 & IPv6
- Extensive RFC compliance, fully featured
- VLAN support up to 4096 VLAN IDs
- Load balancing and failover capabilities

#### UDP & Multicast Offload

- **UDP Sockets API**
- Low user-to-user latency
- Multicast replication on ingress or egress

#### iSCSI

- iSCSI initiator and target mode stack
- CRC32 offload generation verification
- iSCSI proxy switching based on SCSI CDB
- Full HBA offload ٠
- T10 DIF/DIX support

## FCoE

- Full FCoE offload (Initiator or Target)
- Open FCoE offload (Initiator)
- CRC32 offload generation & verification
- Ingress & Egress ACL (Access Control List)
- T10 DIF/DIX support ٠

#### Unified Boot

- PXE, iSCSI and FCoE support
- Legacy and uEFI environments

#### Stateless Offloads

- TCP/UDP IPv4/6 checksum offload
- TSO, LSO and GSO for IPv4 & IPv6
- VLAN filtering, insertion & extraction • Line rate packet filtering and attack protection
- Nanosecond granularity 64b timestamping Ethernet Routing (packet header rewrite)
- Packet Tracing and Packet Sniffing

## Ethernet

- IEEE 802.3ae (10 GbE) IEEE 802.3az Energy Efficient Ethernet

- IEEE 802.3z (1GbE) IEEE 802.1p Priority IEEE 802.1Q VLAN Tagging •
- IEEE 802.1Qbg EVB/VEPA •
- IEEE 802.1BR Bridge Port Extension
- IEEE 802.1Qau Congestion Notification •
- IEEE 802.3x Flow Control ٠
- IEEE 802.3ad Load-balancing & Failover
- Ethernet II and 802.3 encapsulated frames
- Multiple MAC addresses per interface •
- Jumbo Frames up to 9.6 Kbytes

#### Physical and Environmental

- **Fully RoHS Compliant** ٠
- Operating Temp: 0° to 55° C or 32° to 131° F Operating Humidity: 5 to 95%
- Airflow: 200 lf/m •
- Typical power: 11 W Low Profile: H: 2.731" x L: 6.6"